

## Features

- Uses PingWei advanced PerfectMOS5 technology
- Extremely low on-resistance  $R_{DS(on)}$
- Excellent  $Q_g \times R_{DS(on)}$  product(FOM)
- Excellent Low Ciss
- Qualified according to JEDEC criteria

## Benefits

- High robustness and reliability
- Increases maximum current capability
- Low power loss, high power density
- Easy paralleling

## Applications

- Synchronous Rectification for AC/DC Quick Charger
- Battery management
- UPS (Uninterruptible Power Supplies)

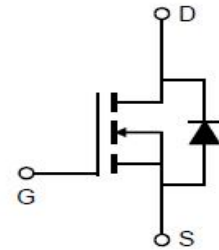
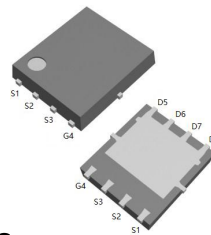


**100% DVDS Tested**  
**100% Avalanche Tested**

## Product Summary

$V_{DS}$	60V
$R_{DS(on)}@10V$ typ	5.3mΩ
$R_{DS(on)}@4.5V$ typ	6.9mΩ
$I_D$	40A

DFN3×3



## Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
PW065N06HSL	065N06HSL	DFN3*3	Tape&Reel	13 inches	12mm	5000pcs

## Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	$V_{DS}$	60	V
Continuous drain current	$I_D$	73	A
$T_C = 25^\circ\text{C}$ (Silicon limit)		40	
$T_C = 25^\circ\text{C}$ (Package limit)		40	
$T_C = 100^\circ\text{C}$ (Silicon limit)		10	
$T_a = 25^\circ\text{C}$			
Pulsed drain current ( $T_C = 25^\circ\text{C}$ , $t_p = 100\mu\text{s}$ )	$I_{D\ pulse}$	160	A
Avalanche energy, single pulse ( $L=0.5\text{mH}$ , $V_{ds}=48\text{V}$ )	$E_{AS}$	45	mJ
Gate-Source voltage	$V_{GS}$	$\pm 20$	V
Power dissipation	$P_{tot}$	61	W
$T_C = 25^\circ\text{C}$		1.2	
$T_a = 25^\circ\text{C}$			
Operating junction and storage temperature	$T_j, T_{stg}$	-55...+150	$^\circ\text{C}$
Soldering temperature, wave soldering only allowed at leads (1.6mm from case for 10s)	$T_{sold}$	260	$^\circ\text{C}$



## Thermal Resistance

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Thermal resistance, junction – Lead.	RthJL	-	-	2.0	°C/W	-
Thermal resistance, junction - ambient(min. footprint)	RthJA	-	-	103	°C/W	-

## Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

## Static Characteristic

Drain-source breakdown voltage	$BV_{DSS}$	60	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Gate threshold voltage	$V_{GS(th)}$	1	-	2.5	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Zero gate voltage drain current	$I_{DSS}$	-	0.02	1	$\mu A$	$V_{DS}=60V, V_{GS}=0V$ $T_j=25^\circ C$ $T_j=150^\circ C$
Gate-source leakage current	$I_{GSS}$	-	$\pm 10$	$\pm 100$	nA	$V_{GS}=\pm 20V, V_{DS}=0V$
Drain-source on-state resistance	$R_{DS(on)}$	-	5.3	6.5	mΩ	$V_{GS}=10V, I_D=20A$ $V_{GS}=4.5V, I_D=20A$
Transconductance	$g_{fs}$	-	30	-	S	$V_{DS}=5V, I_D=18A$

## Dynamic Characteristic

Input Capacitance	$C_{iss}$	-	1653	-	pF	$V_{GS}=0V, V_{DS}=30V,$ $f=1MHz$
Output Capacitance	$C_{oss}$	-	294	-		
Reverse Transfer Capacitance	$C_{rss}$	-	37	-		
Gate Total Charge	$Q_G$	-	27	-	nC	$V_{DS}=30V, I_D=20A,$ $V_{GS}=10V$
Gate-Source charge	$Q_{gs}$	-	6	-		
Gate-Drain charge	$Q_{gd}$	-	4	-		
Turn-on delay time	$t_{d(on)}$	-	8	-	ns	$V_{GS}=10V, V_{DD}=30V,$ $R_{G\_ext}=1.6\Omega, I_D=20A$
Rise time	$t_r$	-	20	-		
Turn-off delay time	$t_{d(off)}$	-	24	-		
Fall time	$t_f$	-	11	-		
Gate resistance	$R_G$	-	1.5	-	Ω	$V_{GS}=0V, V_{DS}=0V,$ $f=1MHz$



## Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	$V_{SD}$	-	-	1.2	V	$V_{GS}=0V, I_{SD}=20A$
Body Diode Continuous Forward Current	$I_S$	-	-	40	A	$TC = 25^{\circ}C$
Body Diode Pulsed Current	$I_S$ pulse	-	-	160	A	$TC = 25^{\circ}C$
Body Diode Reverse Recovery Time	$t_{rr}$	-	37	-	ns	$V_R=30V, I_F=20A,$ $dI/dt=100A/\mu s$
Body Diode Reverse Recovery Charge	$Q_{rr}$	-	18	-	nC	



## Typical Performance Characteristics

Fig 1: Output Characteristics

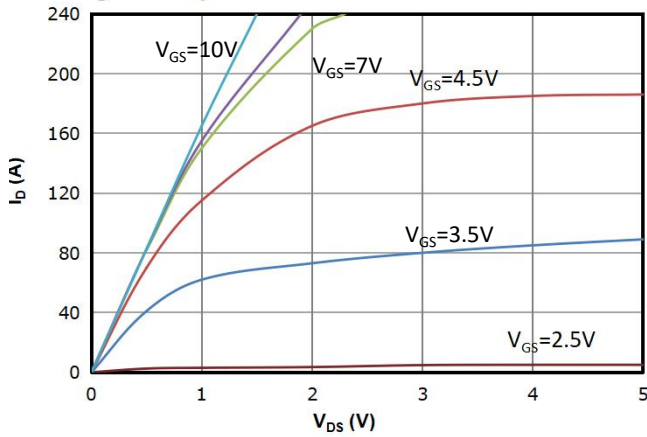


Fig 2: Transfer Characteristics

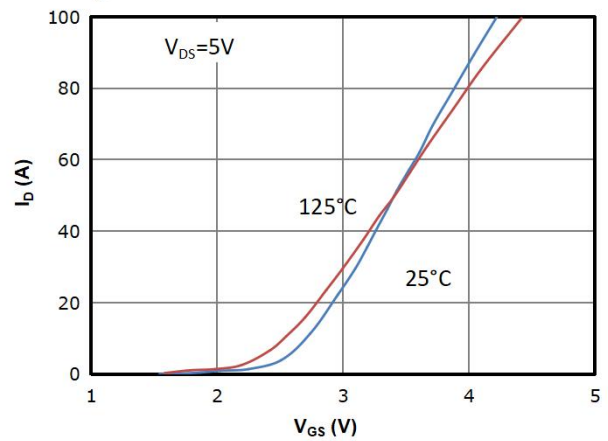


Fig 3:  $R_{DS(on)}$  vs Drain Current and Gate Voltage

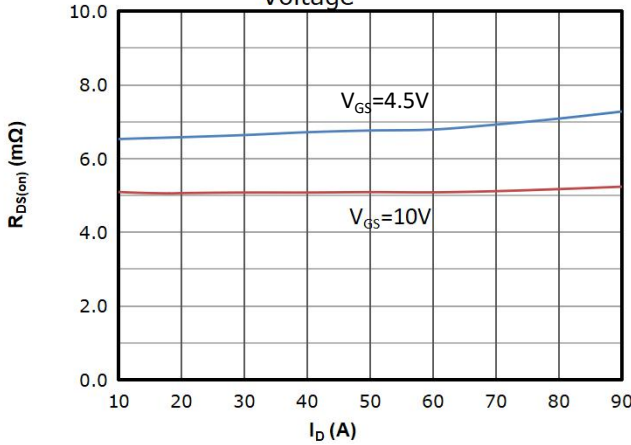


Fig 4:  $R_{DS(on)}$  vs Gate Voltage

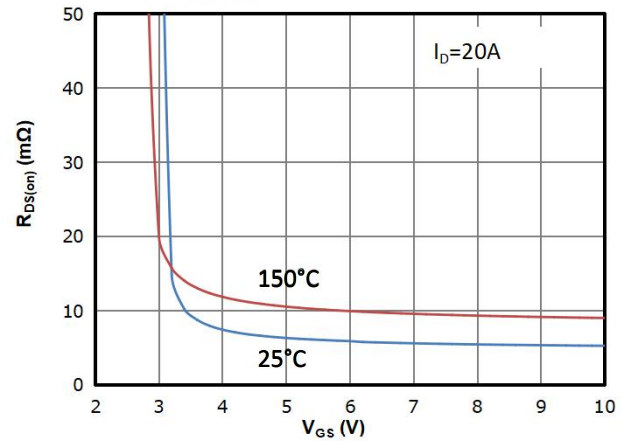


Fig 5:  $R_{DS(on)}$  vs. Temperature

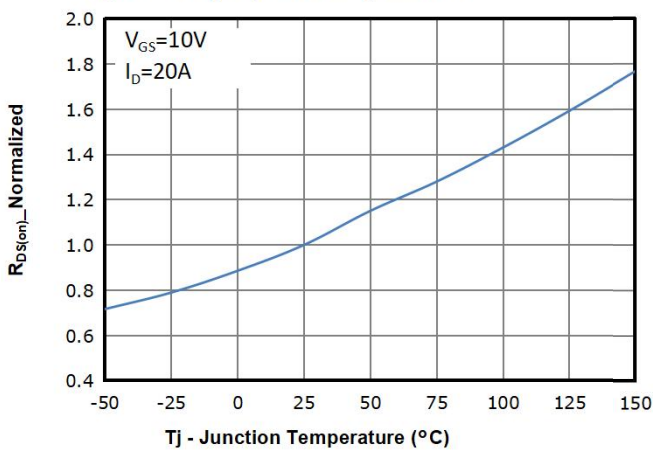


Fig 6:  $V_{GS(th)}$  vs. Temperature

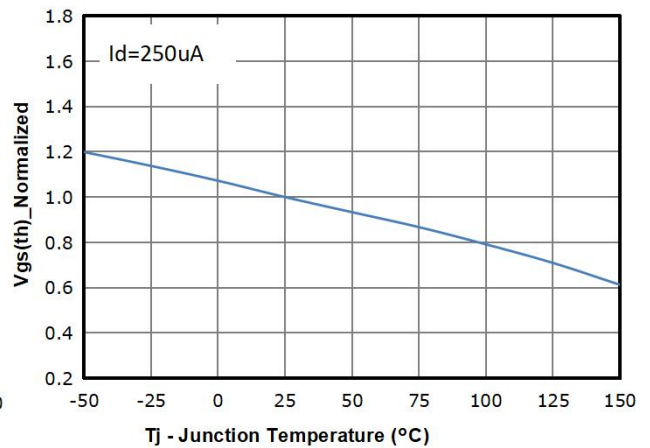




Fig 7: BVdss vs. Temperature

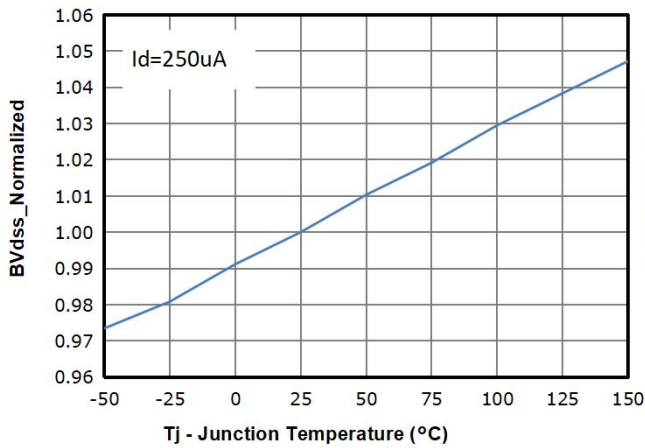


Fig 8: Capacitance Characteristics

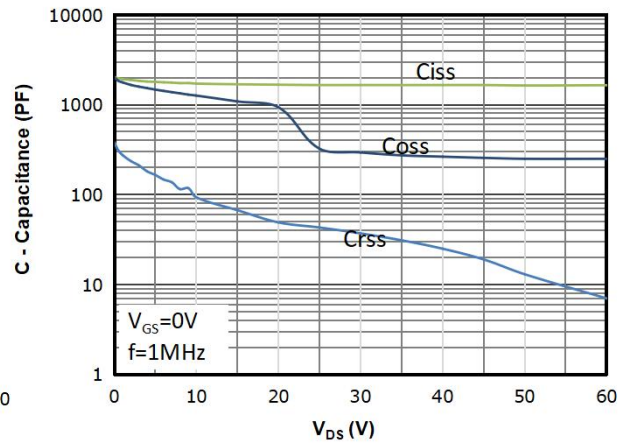


Fig 9: Gate Charge Characteristics

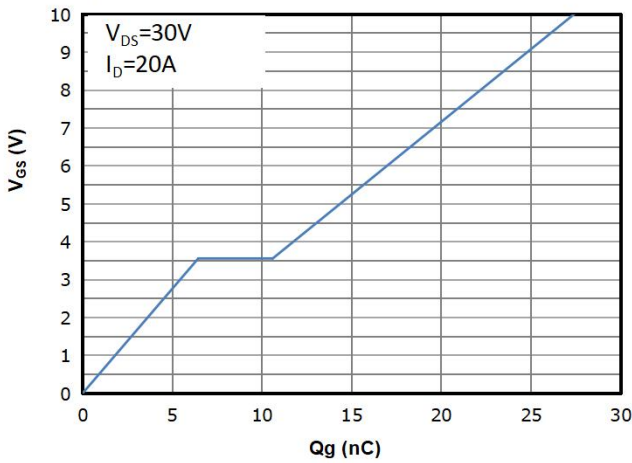


Fig 10: Body-diode Forward Characteristics

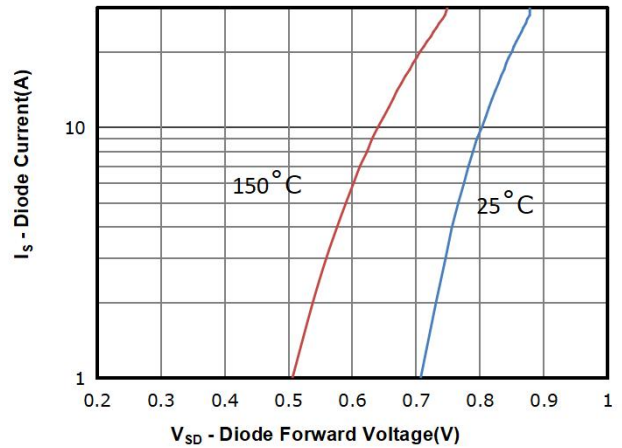


Fig 11: Power Dissipation

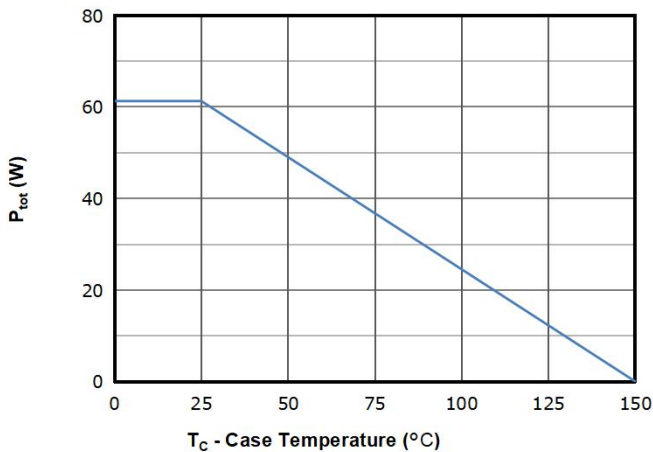


Fig 12: Drain Current Derating

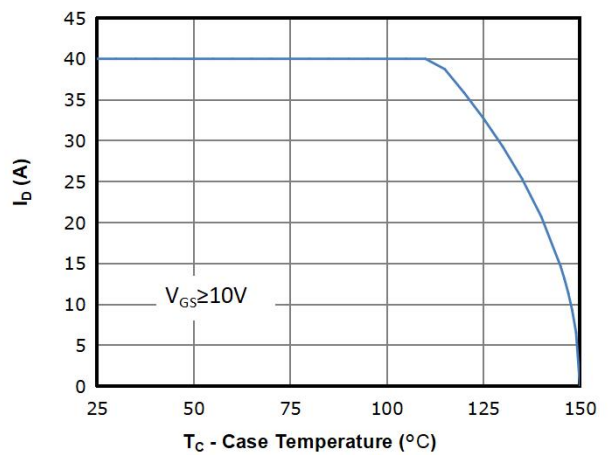




Fig 13: Safe Operating Area

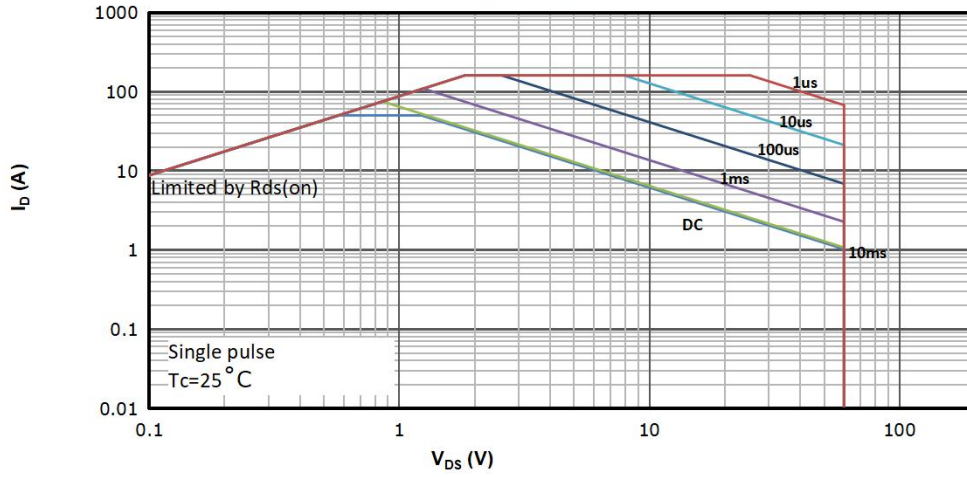
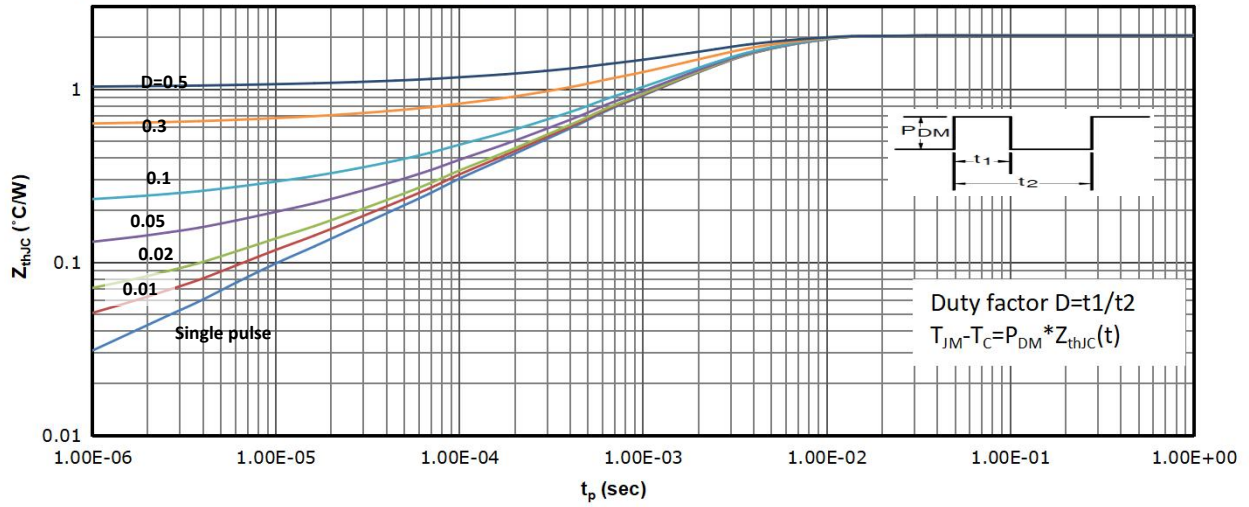
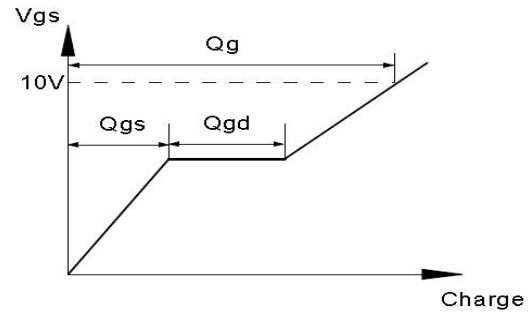
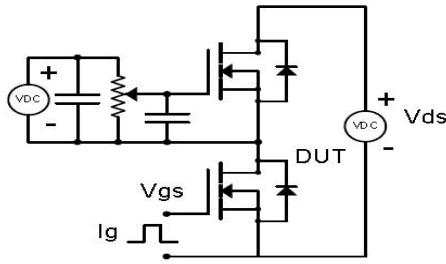


Fig 14: Max. Transient Thermal Impedance

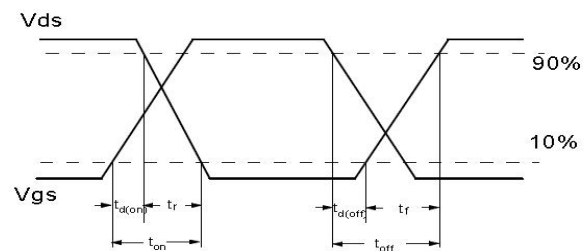
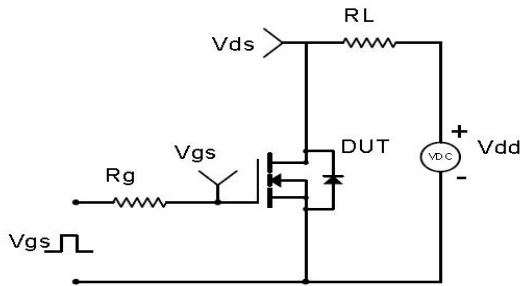


## Test Circuit & Waveform

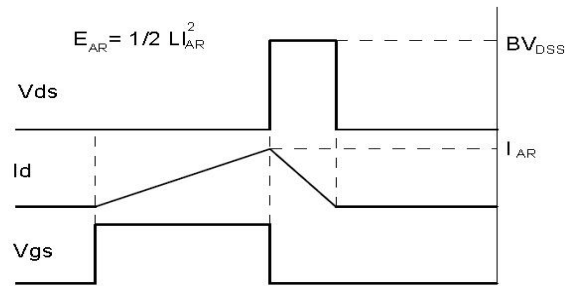
Gate Charge Test Circuit & Waveform



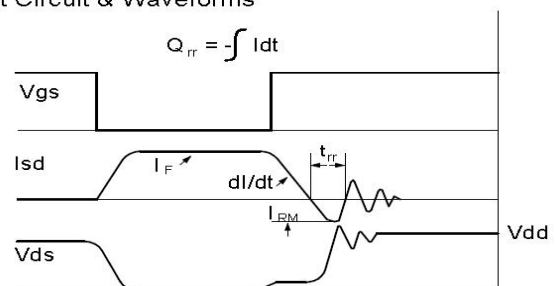
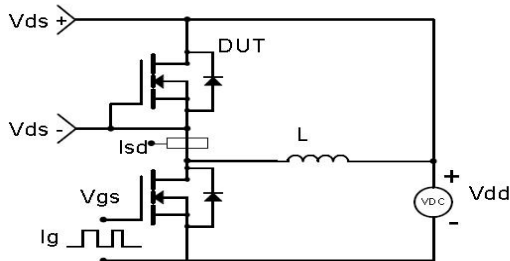
Resistive Switching Test Circuit & Waveforms



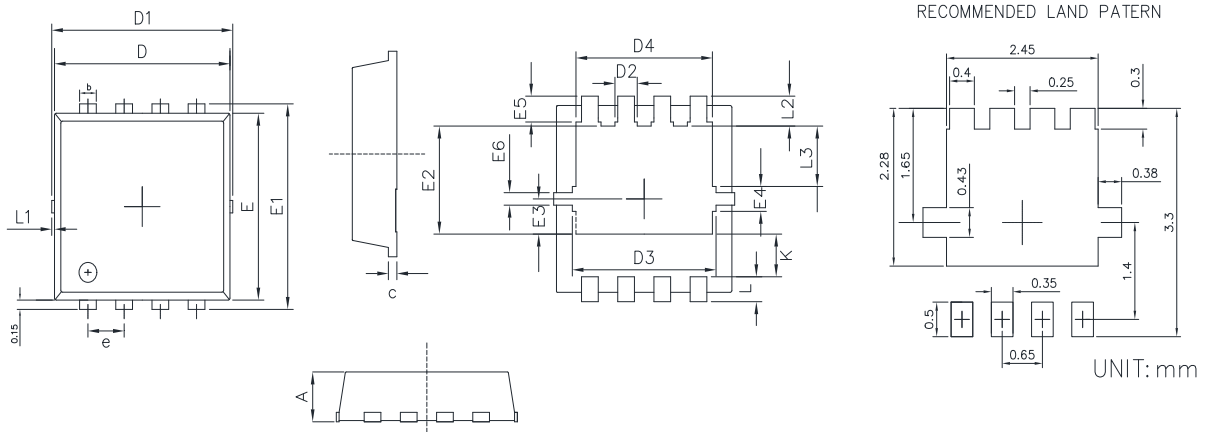
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



## Package Outline: DFN3X3



SYMBOL	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.70	1.00	0.028	0.039
b	0.24	0.40	0.009	0.016
c	0.10	0.25	0.004	0.010
D	3.00	3.25	0.118	0.128
D1	3.10	3.50	0.122	0.138
D2	0.30	0.50	0.012	0.020
D3	2.50	2.70	0.098	0.106
D4	2.35	2.55	0.093	0.100
E	2.90	3.10	0.114	0.122
E1	3.15	3.45	0.124	0.136
E2	1.65	1.85	0.065	0.073
E3	0.48	0.68	0.019	0.027
E4	0.23	0.50	0.009	0.020
E5	0.20	0.40	0.008	0.016
E6	0.08	0.25	0.003	0.010
e	0.55	0.75	0.022	0.030
K	0.52	0.82	0.020	0.032
L	0.25	0.55	0.010	0.022
L1	0.00	0.10	0.000	0.004
L2	0.28	0.58	0.011	0.023
L3	0.88	1.08	0.035	0.043





## Revision History

Revision	Date	Major changes
1.0	2023/7/18	Release of Formal Version.

## Disclaimer

Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

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